

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
0	-			-				
1	-			-				
2	-			-				
3	-			-				
4	-			-				
5	-			-				
6	-			-				
7	-			-				
8	-			-				
9	-			-				
10	Error Log - Most recent	40010	Register	Read only	Integer			Error Log 1 Module number
11	Error Log - Most recent	40011	Register	Read only	Integer			Error Log 1 Line number
12	Error Log	40012	Register	Read only	Integer			Error Log 2 Module number
13	Error Log	40013	Register	Read only	Integer			Error Log 2 Line number
14	Error Log	40014	Register	Read only	Integer			Error Log 3 Module number
15	Error Log	40015	Register	Read only	Integer			Error Log 3 Line number
16	Error Log	40016	Register	Read only	Integer			Error Log 4 Module number
17	Error Log	40017	Register	Read only	Integer			Error Log 4 Line number
18	Error Log	40018	Register	Read only	Integer			Error Log 5 Module number
19	Error Log	40019	Register	Read only	Integer			Error Log 5 Line number
20	Error Log	40020	Register	Read only	Integer			Error Log 6 Module number
21	Error Log	40021	Register	Read only	Integer			Error Log 6 Line number
22	Error Log	40022	Register	Read only	Integer			Error Log 7 Module number
23	Error Log	40023	Register	Read only	Integer			Error Log 7 Line number
24	Error Log -Oldest	40024	Register	Read only	Integer			Error Log 8 Module number
25	Error Log -Oldest	40025	Register	Read only	Integer			Error Log 8 Line number
26	-			-				
27	Hardware configuration	40027	Register	Read only	Integer			Equivalent to Hardware config in uMatrixWin Utilities
28	-			-				
29	-			-				
30	Scaled Line Voltage	40030	Register	Read only	Float	180.00	0.00	Divide by 100 in Citect before displaying
31	Scaled Line current	40031	Register	Read only	Float	6.82	0.00	Divide by 100 in Citect before displaying
32	Current Delay Time	40032	Register	Read only	Float	300.00	0.00	Divide by 100 in Citect before displaying. Shows target time for current delay period
33	LDC Compensation Voltage	40033	Register	Read only	Float	180.00	0.00	Divide by 100 in Citect before displaying. LDC Compensation for setpoint voltage.
34	Line Phase	40034	Register	Read only	Float	180.00	-180.00	Divide by 100 in Citect before displaying. Phase angle from Line Voltage to Line Current in degrees
35	-			-				
36	-			-				
37	-			-				
38	-			-				
39	-			-				
40	Backlight dwell time	40040	Register	Read/Write	Float	65.500	0.000	Divide by 1000 in Citect before displaying. Value scaled for 65.5 sec maximum backlight dwell time after all activity ceases.
41	-			-				
42	Modbus address	40042	Register	Read/Write	Integer	247	1	
43	-			-				

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44	-			-				
45	-			-				
46	-			-				
47	-			-				
48	-			-				
49	-			-				
100	Unfiltered Secondary Voltage	40100	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Value scaled for 110V nominal secondary voltage
101	Secondary Current	40101	Register	Read only	Float	1.55	0.00	Divide by 100 in Citect before displaying. Value scaled for 1.00A nominal secondary current
102	-			-				
103	-			-				
104	-			-				
105	-			-				
106	-			-				
107	Temperature	40107	Register	Read only	Float	93.90	-3.50	Divide by 100 in Citect before displaying. Value in Celcius
108	Line voltage Frequency	40108	Register	Read only	Float	327.67	7.63	Divide by 100 in Citect before displaying. Value in Hertz
109	Line current Frequency	40109	Register	Read only	Float	327.67	7.63	Divide by 100 in Citect before displaying. Value in Hertz
110	-			-				
111	-			-				
112	Fine Upper Threshold	40112	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Fine Bandwidth upper threshold
113	Fine Lower Threshold	40113	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Fine Bandwidth lower threshold
114	Coarse Upper Threshold	40114	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Coarse Bandwidth upper threshold
115	Coarse Lower Threshold	40115	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Coarse Bandwidth lower threshold
116	Lower Error	40116	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Voltage minus Setpoint.
117	Raise Error	40117	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Setpoint minus Secondary Voltage
118	Compensated Setpoint	40118	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Setpoint plus Line Drop Compensation
119	-			-				
120	Fine Hysteresis	40120	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Fine Bandwidth hysteresis
121	Coarse Hysteresis	40121	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary Coarse Bandwidth hysteresis
122	-			-				
123	Load Step 1	40123	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Load Step 1 Secondary Voltage
124	Load Step 2	40124	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Load Step 2 Secondary Voltage
125	LDC Resistive voltage	40125	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. LDC Resistive compensation voltage
126	LDC Reactive voltage	40126	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. LDC Reactive compensation voltage
127	-			-				
128	Error Voltage	40128	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Current Secondary voltage deviation from Setpoint.
129	-			-				
130	-			-				
131	Load Step Voltage	40131	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Load Step Secondary Voltage compensation
132	-			-				
133	Delay Time	40133	Register	Read only	Float	320.00	0.00	Divide by 100 in Citect before displaying. Currently selected delay time target
134	-			-				
135	-			-				
136	-			-				
137	-			-				
138	-			-				
139	-			-				

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140	-			-				
141	-			-				
142	-			-				
143	-			-				
144	-			-				
145	-			-				
146	-			-				
147	-			-				
148	-			-				
149	-			-				
150	-			-				
151	-			-				
152	-			-				
153	-			-				
154	-			-				
155	-			-				
156	-			-				
157	-			-				
158	-			-				
159	-			-				
160	-			-				
161	-			-				
162	-			-				
163	-			-				
164	-			-				
165	-			-				
166	-			-				
167	-			-				
168	-			-				
169	-			-				
170	-			-				
171	-			-				
172	-			-				
173	-			-				
174	-			-				
175	-			-				
176	-			-				
177	-			-				
178	-			-				
179	-			-				
180	-			-				
181	-			-				
182	-			-				
183	-			-				
184	-			-				
185	-			-				

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
186	-			-				
187	-			-				
188	-			-				
189	-			-				
190	-			-				
191	-			-				
192	-			-				
193	-			-				
194	Overcurrent Block threshold	40194	Register	Read/Write	Integer	150	50	Secondary Overcurrent block threshold (% of Nominal)
195	-			-				
196	Tap Rate Alarm threshold	40196	Register	Read/Write	Float	100.00	0.00	Divide by 100 in Citect before displaying. Tape Rate Alarm value in Taps/hour
197	-			-				
198	Overvoltage Alarm threshold	40198	Register	Read/Write	Float	140.00	0.00	Divide by 100 in Citect before displaying. Secondary Overvoltage alarm threshold
199	Overvoltage Alarm hysteresis	40199	Register	Read/Write	Float	1.00	0.00	Divide by 100 in Citect before displaying. Secondary Overvoltage alarm hysteresis
200	Undervoltage Block threshold	40200	Register	Read/Write	Float	90.00	0.00	Divide by 100 in Citect before displaying. Secondary Undervoltage block threshold
201	Undervoltage Block hysteresis	40201	Register	Read/Write	Float	5.00	0.00	Divide by 100 in Citect before displaying. Secondary Undervoltage block hysteresis
202	-			-				
203	Delay reset time	40203	Register	Read/Write	Float	1.00	0.00	Divide by 100 in Citect before displaying. Currently selected delay reset time
204	-			-				
205	-			-				
206	-			-				
207	-			-				
208	-			-				
209	-			-				
210	Tap Fail Delay time	40210	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Tap Fail Delay time target
211	Tap Fail Delay reset time	40211	Register	Read/Write	Float	1.00	0.00	Divide by 100 in Citect before displaying. Tap Fail Delay reset time
212	Overcurrent Delay time	40212	Register	Read/Write	Float	60.00	0.00	Divide by 100 in Citect before displaying. Overcurrent Delay time target
213	Overcurrent Delay reset time	40213	Register	Read/Write	Float	1.00	0.00	Divide by 100 in Citect before displaying. Overcurrent Delay reset time
214	Overvoltage Delay time	40214	Register	Read/Write	Float	60.00	0.00	Divide by 100 in Citect before displaying. Overvoltage Delay time target
215	Overvoltage Delay reset time	40215	Register	Read/Write	Float	1.00	0.00	Divide by 100 in Citect before displaying. Overvoltage Delay reset time
216	Undervoltage Delay time	40216	Register	Read/Write	Float	60.00	0.00	Divide by 100 in Citect before displaying. Undervoltage Delay time target
217	Undervoltage Delay reset time	40217	Register	Read/Write	Float	1.00	0.00	Divide by 100 in Citect before displaying. Undervoltage Delay reset time
218	-			-				
219	-			-				
220	-			-				
221	-			-				
222	-			-				
223	-			-				
224	-			-				
225	-			-				
226	Fine Hysteresis divider	40226	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Fine bandwidth hysteresis divider
227	Coarse Hysteresis divider	40227	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Coarse bandwidth hysteresis divider
228	-			-				
229	-			-				

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230	-			-				
231	-			-				
232	-			-				
233	-			-				
240	-			-				
241	-			-				
242	-			-				
243	-			-				
244	-			-				
245	-			-				
246	-			-				
247	Filtered Secondary Voltage	40247	Register	Read only	Float	146.00	0.00	Divide by 100 in Citect before displaying. Value scaled for 110V nominal secondary voltage
248	-			-				
249	-			-				
250	-			-				
251	-			-				
252	-			-				
253	-			-				
254	-			-				
255	-			-				
256	Tap Rate	40256	Register	Read only	Float	320.00	0.00	Divide by 100 in Citect before displaying. Tape Rate for previous 15mins (Taps/Hr)
257	Max Tap	40257	Register	Read only	Integer	30	1	Max Tap position since last reset
258	Min Tap	40258	Register	Read only	Integer	30	1	Min Tap position since last reset
259	Tap count	40259	Register	Read only	Integer	32768	0	Number of tap changes since last reset.
260	Elapsed time	40260	Register	Read only	Float	288.00	0.00	Divide by 100 in Citect before displaying. Elapsed time since last reset. Displayed in hours or days
261	-			-				
262	-			-				
263	-			-				
264	Current Tap Position	40264	Register	Read only	Integer	30	1	Current tap position - scaled if Analogue TPI
265	-			-				
266	-			-				
267	Rate tap count	40267	Register	Read only	Integer	32768	0	Number of tap changes in current 15mins.
268	-			-				
269	-			-				
556	V out Upper	40556	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Value corresponding to 20mA from Volts output
557	V out Lower	40557	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Value corresponding to 4mA from Volts output
558	VT scale	40558	Register	Read/Write	Float	132.00	0.11	Line voltage multiplier (x1kVolt)
559	CT Scale	40559	Register	Read/Write	Integer	6000	1	Line current multiplier (x1Amp)
560	Setpoint	40560	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary setpoint voltage
561	Fine Bandwidth	40561	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary fine bandwidth voltage
562	Coarse Bandwidth	40562	Register	Read/Write	Float	146.00	0.00	Divide by 100 in Citect before displaying. Secondary coarse bandwidth voltage
563	Tap Range	40563	Register	Read/Write	Integer	30	15	Analogue TPI tap range. Set to 30 for Digital TPI
564	Load Step 1	40564	Register	Read/Write	Float	100.00	-100.00	Divide by 100 in Citect before displaying. Load step 1 (% of setpoint)
565	Load Step 2	40565	Register	Read/Write	Float	100.00	-100.00	Divide by 100 in Citect before displaying. Load step 2 (% of setpoint)
566	Initial Delay	40566	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Initial Delay time

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567	Interval Delay	40567	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Interval Delay time
568	Coarse Delay	40568	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Coarse Delay time
569	LDC Resistive compensation	40569	Register	Read/Write	Float	100.00	-100.00	Divide by 100 in Citect before displaying. Secondary % Resistive compensation @ 1A
570	LDC Reactive compensation	40570	Register	Read/Write	Float	100.00	-100.00	Divide by 100 in Citect before displaying. Secondary % Reactive compensation @ 1A
571	Tap Change Pulse width	40571	Register	Read/Write	Float	320.00	0.00	Divide by 100 in Citect before displaying. Tap change Pulse width.
572	-			-				
573	-			-				
574	-			-				
575	LDC Z-Comp compensation	40575	Register	Read/Write	Float	15.00	0.00	Divide by 100 in Citect before displaying. Z-Comp % compensation @ 1A
576	-			-				
577	-			-				
578	-			-				
579	Voltage Calibration Adjust	40579	Register	Read/Write	Float	2.00	-2.00	Divide by 100 in Citect before displaying. Voltage Calibration Adjust in V
600	-			-				
601	Auto/Manual switch state	00601	Coil	Read only	Bit	TRUE	FALSE	Auto/Manual mode switch logical state. True if Manual, false if Auto.
602	Loadshed 1 switch state	00602	Coil	Read only	Bit	TRUE	FALSE	Loadshed 1 selector switch logical state
603	Loadshed 2 switch state	00603	Coil	Read only	Bit	TRUE	FALSE	Loadshed 2 selector switch logical state
604	-			-				
605	-			-				
606	-			-				
607	-			-				
608	"SET" Key State	00608	Coil	Read only	Bit	TRUE	FALSE	True if "SET" Key pressed
609	"DATA" Key State	00609	Coil	Read only	Bit	TRUE	FALSE	True if "DATA" Key pressed
610	"UP" Key State	00610	Coil	Read only	Bit	TRUE	FALSE	True if "UP" Key pressed
611	"DOWN" Key State	00611	Coil	Read only	Bit	TRUE	FALSE	True if "DOWN" Key pressed
612	"SELECT" Key State	00612	Coil	Read only	Bit	TRUE	FALSE	True if "SELECT" Key pressed
613	Manual Tap Lower	00613	Coil	Read/Write	Bit	TRUE	FALSE	Rests false. Must be pulsed true for at least 40msec to produce a manual tap lower.
614	Manual Tap Raise	00614	Coil	Read/Write	Bit	TRUE	FALSE	Rests false. Must be pulsed true for at least 40msec to produce a manual tap raise.
615	Reset Tap Log	00615	Coil	Read/Write	Bit	TRUE	FALSE	Rests false. Must be pulsed true for at least 40msec to reset tap change log.
616	Lower request	00616	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage is greater than setpoint plus fine bandwidth plus load shed voltage
617	Raise request	00617	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage is less than setpoint minus fine bandwidth plus load shed voltage
618	Coarse lower request	00618	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage is greater than setpoint plus coarse bandwidth plus load shed voltage
619	Coarse raise request	00619	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage is less than setpoint minus coarse bandwidth plus load shed voltage
620	Initial > Inverse	00620	Coil	Read only	Bit	TRUE	FALSE	True if Initial delay is greater than the inverse initial delay
621	-			-				
622	-			-				
623	-			-				
624	-			-				
625	-			-				
626	-			-				
627	-			-				
628	Overcurrent block exceeded	00628	Coil	Read only	Bit	TRUE	FALSE	True if Secondary current exceeds the Overcurrent block
629	Tap Rate exceeded	00629	Coil	Read only	Bit	TRUE	FALSE	True if Tap Rate exceeded
630	Overvoltage alarm exceeded	00630	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage exceeds Overvoltage alarm

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631	Undervoltage block exceeded	00631	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage is less than the Undervoltage block
648	Delay timing	00648	Coil	Read only	Bit	TRUE	FALSE	True if delay currently timing
649	Delay Expired	00649	Coil	Read only	Bit	TRUE	FALSE	True if delay currently expired but not restarted or reset
650	-			-				
651	-			-				
652	Tap change pulse timing	00652	Coil	Read only	Bit	TRUE	FALSE	True during Tap change pulse width time
653	-			-				
654	-			-				
655	-			-				
656	Tap Fail timing	00656	Coil	Read only	Bit	TRUE	FALSE	True if Tap Fail delay currently timing
657	Tap Fail Expired	00657	Coil	Read only	Bit	TRUE	FALSE	True if Tap Fail delay currently expired but not restarted or reset
658	Overcurrent Delay Timing	00658	Coil	Read only	Bit	TRUE	FALSE	True if Overcurrent delay currently timing
659	Overcurrent Delay Expired	00659	Coil	Read only	Bit	TRUE	FALSE	True if Overcurrent delay currently expired but not restarted or reset
660	Overvoltage Delay timing	00660	Coil	Read only	Bit	TRUE	FALSE	True if Overvoltage delay currently timing
661	Overvoltage Delay Expired	00661	Coil	Read only	Bit	TRUE	FALSE	True if Overvoltage delay currently expired but not restarted or reset
662	Undervoltage Delay timing	00662	Coil	Read only	Bit	TRUE	FALSE	True if Undervoltage delay currently timing
663	Undervoltage Delay Expired	00663	Coil	Read only	Bit	TRUE	FALSE	True if Undervoltage delay currently expired but not restarted or reset
664	Fine Bandwidth exceeded	00664	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage outside fine bandwidth window
665	Coarse Bandwidth exceeded	00665	Coil	Read only	Bit	TRUE	FALSE	True if Secondary voltage outside coarse bandwidth window
666	-			-				
667	-			-				
668	-			-				
669	-			-				
670	Manual Tap Change	00670	Coil	Read only	Bit	TRUE	FALSE	True if Manual Raise or Lower is being actioned.
671	Operate	00671	Coil	Read only	Bit	TRUE	FALSE	True if not in Manual mode and Secondary voltage is greater than the Undervoltage Block and the Secondary Current is less than the Overcurrent Block
672	Lower timing	00672	Coil	Read only	Bit	TRUE	FALSE	True if delay timing for a tap lower
673	Lower request	00673	Coil	Read only	Bit	TRUE	FALSE	True if a tap lower output is requested
674	Raise timing	00674	Coil	Read only	Bit	TRUE	FALSE	True if delay timing for a tap raise
675	Raise request	00675	Coil	Read only	Bit	TRUE	FALSE	True if a tap raise output is requested
676	No action	00676	Coil	Read only	Bit	TRUE	FALSE	True if secondary voltage is within fine bandwidth window or in Manual mode
677	-			-				
678	-			-				
679	-			-				
680	Tap Change	00680	Coil	Read only	Bit	TRUE	FALSE	True while tap change requested but no feedback
681	-			-				
682	-			-				
683	Initial Expired	00683	Coil	Read only	Bit	TRUE	FALSE	True after initial delay has expired until secondary voltage is back within fine bandwidth window
684	Manual Tap Lower	00684	Coil	Read only	Bit	TRUE	FALSE	True Manual Tap Lower is being actioned.
685	Manual Tap Raise	00685	Coil	Read only	Bit	TRUE	FALSE	True Manual Tap Raise is being actioned.
686	-			-				
687	-			-				
688	-			-				
689	-			-				
690	-			-				

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691	-			-				
692	-			-				
693	-			-				
694	-			-				
695	-			-				
760	-			-				
761	-			-				
762	-			-				
763	-			-				
764	-			-				
765	-			-				
766	-			-				
767	-			-				
768	-			-				
769	-			-				
770	-			-				
771	-			-				
772	-			-				
773	-			-				
774	-			-				
775	-			-				
776	-			-				
777	-			-				
778	-			-				
779	-			-				
780	-			-				
781	-			-				
782	-			-				
783	-			-				
784	-			-				
785	-			-				
786	-			-				
787	-			-				
788	-			-				
789	-			-				
790	-			-				
791	-			-				
808	-			-				
809	-			-				
810	-			-				
811	-			-				
812	-			-				
813	-			-				
814	-			-				
815	-			-				
816	-			-				

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817	-			-				
818	-			-				
819	-			-				
820	-			-				
821	-			-				
822	-			-				
823	-			-				
880	Delay Mode	00880	Coil	Read/Write	Bit	TRUE	FALSE	True if initial delay mode is Normal, else Inverse
881	LDC mode 0	00881	Coil	Read/Write	Bit	TRUE	FALSE	LSB
882	LDC mode 1	00882	Coil	Read/Write	Bit	TRUE	FALSE	LDC mode bits
883	LDC mode 2	00883	Coil	Read/Write	Bit	TRUE	FALSE	MSB
884	TPI Type	00884	Coil	Read/Write	Bit	TRUE	FALSE	True if Digital TPI used, else Analogue TPI
885	-			-				
886	-			-				
887	-			-				
888	-			-				
889	O/C Block On or Off	00889	Coil	Read/Write	Bit	TRUE	FALSE	True if O/C Block is Off. False if O/C Block is On.
890	-			-				
891	-			-				
892	-			-				
893	-			-				
894	-			-				
895	-			-				
896	-			-				
897	-			-				
898	-			-				
899	-			-				
900	-			-				
901	-			-				
902	-			-				
903	-			-				
920	Host Parity Enable	00920	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking enabled for the programming port
921	Host Parity Odd/Even	00921	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking is set to Odd for the programming port
922	Host Baud 0	00922	Coil	Read/Write	Bit	TRUE	FALSE	Programming port baud rate selector
923	Host Baud 1	00923	Coil	Read/Write	Bit	TRUE	FALSE	Programming port baud rate selector
924	Host Baud 2	00924	Coil	Read/Write	Bit	TRUE	FALSE	Programming port baud rate selector
925	Host data bits	00925	Coil	Read/Write	Bit	TRUE	FALSE	True if the programming port uses 7 data bits, else 8 data bits
926	Host stop bits	00926	Coil	Read/Write	Bit	TRUE	FALSE	True if the programming port uses 2 stop bits, else 1 stop bit
927	-			-				
928	Modbus Parity Enable	00928	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking enabled for the Modbus port
929	Modbus Parity Odd/Even	00929	Coil	Read/Write	Bit	TRUE	FALSE	True if Parity checking is set to Odd for the Modbus port
930	Modbus Baud 0	00930	Coil	Read/Write	Bit	TRUE	FALSE	Modbus port baud rate selector
931	Modbus Baud 1	00931	Coil	Read/Write	Bit	TRUE	FALSE	Modbus port baud rate selector
932	Modbus Baud 2	00932	Coil	Read/Write	Bit	TRUE	FALSE	Modbus port baud rate selector
933	Modbus data bits	00933	Coil	Read/Write	Bit	TRUE	FALSE	True if the Modbus port uses 7 data bits, else 8 data bits
934	Modbus stop bits	00934	Coil	Read/Write	Bit	TRUE	FALSE	True if the Modbus port uses 2 stop bits, else 1 stop bit

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
935	-			-				
936	Modbus CDB save	00936	Coil	Read/Write	Bit	TRUE	FALSE	Saves current CDB when pulsed True then False
960	-			-				
961	-			-				
962	-			-				
963	-			-				
964	-			-				
965	-			-				
966	-			-				
967	Elapsed time units	00967	Coil	Read only	Bit	TRUE	FALSE	Elapsed time in Hours if false, in Days if true
968	-			-				
969	-			-				
970	-			-				
971	-			-				
972	-			-				
973	-			-				
974	-			-				
975	-			-				
976	Coarse Selected	00976	Coil	Read only	Bit	TRUE	FALSE	True if Coarse delay currently selected
977	Initial Selected	00977	Coil	Read only	Bit	TRUE	FALSE	True if Initial delay currently selected, else Interval delay
978	Balanced Selected	00978	Coil	Read only	Bit	TRUE	FALSE	True if system balanced - no delay active
979	Blocking Delay Timing	00658	Coil	Read only	Bit	TRUE	FALSE	True if Overcurrent or Undervoltage Blocking delay currently timing
980	Blocking Delay Expired	00659	Coil	Read only	Bit	TRUE	FALSE	True if Overcurrent or Undervoltage Blocking delay currently expired but not restarted or reset.
981	-			-				
982	-			-				
983	-			-				
984	-			-				
985	-			-				
986	-			-				
987	-			-				
988	-			-				
989	-			-				
990	-			-				
991	-			-				
992	-			-				
993	-			-				
994	-			-				
995	-			-				
996	-			-				
997	-			-				
998	-			-				
999	-			-				
1000	Relay Serial Number	41000	String	Read only	String	-	-	Occupies 5 integer registers
1005	Relay Hardware Configuration	41005	String	Read only	String	-	-	Occupies 2 integer registers
1007	BIOS Version	41007	String	Read only	String	-	-	Occupies 3 integer registers

CDB REGISTER	CONTENTS	MODBUS REGISTER	MODBUS TYPE	ACCESS	DATA TYPE	MAX VALUE	MIN VALUE	COMMENT
1010	Software Model	41010	String	Read only	String	-	-	Occupies 7 integer registers
1017	CDB Name	41017	String	Read only	String	-	-	Occupies 8 integer registers
1025	Software Version	41025	String	Read only	String	-	-	Occupies 3 integer registers