

# 2V164C

## Relay Software

### Links to Other Documents

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User Guide: [http://www.rmspl.com.au/userguide/2v164\\_user\\_guide.pdf](http://www.rmspl.com.au/userguide/2v164_user_guide.pdf)

Software Downloads: <http://www.rmspl.com.au/digital/umatrix.htm>

### Version Control

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Issue	Date	Summary of changes
U	03/03/2016	Connection diagrams updated
Q	21/08/2009	UMX Updated
R	01/04/2010	UMX Updated
S	14/04/2011	UMX updated
T	30/09/2015	UMX updated

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DWB	DWB	DWB	



## 1.0 OVERVIEW

The 2V164 Voltage Regulator Relay continuously monitors the transformer output voltage & current. It provides "RAISE" & "LOWER" control commands to the on-load tap changer such that the load centre is automatically maintained within acceptable limits. Small variations in supply frequency will not affect the system performance.

The 2V164 relay is built on the Micro MATRIX digital platform. The standard Micro MATRIX human machine interface (HMI) is combined with fully solid-state voltage sensing & measuring circuitry to provide high accuracy, simple set up & flexible operation. Self-monitoring is carried out by hardware & software watchdogs.

The TPI inputs, output relays & opto isolated status inputs form the essential barriers against high voltage line transients while a switchmode auxiliary supply provides a wide operating range.

A programming port is provided for ease of establishing relay settings using a PC & ~MATRIXwin.

## 2.0 SOFTWARE VERSION CONTROL REGISTER

The following table is a register of the changes for the **UMX2V164C.umx** file.

DATE	SOFTWARE	CHANGES	BIOS	FIRST HARDWARE
21/01/2002	02.02	Initial release of C UMX.	03.11+	2V164K1
18/07/2002	02.14	Over current block added	03.11+	
28/08/2002	02.15	LDC function revised	03.11+	
07/09/2002	02.16	uMatrixWin display Tap Rate in floating point	03.11 – 03.14+	
12/12/2002	04.01	New hardware and Bios	04.xx	
04/01/2003	04.10	Put elapsed time in live CDB	04.xx	
28/04/2003	04.30	Quadrature LDC calculation bug fix	04.xx	
20/05/2003	05.00	Feature upgrade	05.xx	
20/06/2003	05.01	Delay timing reset revised	05.xx	
26/05/2004	05.10	Move User parameters to static registers	05.xx	
02/02/2005	05.11	Set MAXTAPS lower limit to 2	05.xx	
30/04/2005	05.20	Add Z-Comp to LDC Menu	05.xx	
22/08/2006	05.21	Voltage calibration adjustment added	05.xx	
05/10/2006	05.22	Save Voltage Calibration in EEROM	05.xx	
27/10/2006	05.23	Fix error switching from "TPI Data" to "Config" after power up	05.xx	
17/08/2009	05.24	Update Phase correction algorithm	05.xx	
29/03/2010	05.30	CR0924 - Improve Current channel linearity	05.xx	
12/04/2011	05.40	CR0988 - Add TPI failure detection and reporting	05.xx	
30/09/2015	05.45	Line current scaling added	05.xx	



### **3.0 DESCRIPTION – UMX2V164CUMX**

#### **3.1 Standard Features**

The 2V164 provides a range of standard features as described in the RMS Technical Bulletin that may be downloaded from:

[www.rmspl.com.au/handbook/2v164.pdf](http://www.rmspl.com.au/handbook/2v164.pdf)

#### **3.2 Special Features of this Software UMX Version**

TPI feedback mode:

When a tap change command is output to the OLTC, the tap change fail timer is started. If a single tap change restores the sensed voltage to a balanced condition the relay is reset. If the sensed voltage remains in error the interval time delay will start based on one of the three methods described below: The required operating mode is selected using the UMX order code.

##### **TPI FEEDBACK METHOD          UMX2V164C**

In this mode the 2V200 TPI transducer must be connected as per figure 2. The control sequence is as follows:

1. A voltage deviation starts the initial time delay.
2. The time delay expires & a tap change command is output.
3. The tap change contact will remain closed until a signal is received from the TPI transducer confirming that a tap change event has occurred.
4. The interval time delay is initiated.
5. Sequence 2 to 4 will repeat at the rate determined by the Interval timer setting until the sensed voltage has moved back to a balanced condition.

For information on the two other methods, see the 2V164 data sheet or the UMX2V164A & UMX2V164B documents.



#### 4.0 USER INTERFACE.

Refer to the ~MATRIX Users Guide for detailed instructions on the operation of the user interface.

To download a PDF version of the guide uMatrix: [www.rmspl.com.au/digital/umatrixinfo.pdf](http://www.rmspl.com.au/digital/umatrixinfo.pdf)

To download a PDF version of the guide uMatrix-S: [www.rmspl.com.au/digital/umatrix-s\\_info.pdf](http://www.rmspl.com.au/digital/umatrix-s_info.pdf)

To download USB VCP drivers for uMatrix-S: [www.rmspl.com.au/umatrix.htm](http://www.rmspl.com.au/umatrix.htm)

#### 5.0 LOAD SOFTWARE

Loading a UMX requires at least the following:

A PC with Windows 2000 or later and at least one COM (uMatrix) or USB (uMatrix-S) port.

(Note: USB comms require drivers to be installed on your PC. See section 4.0 downloads.)

UMatrixWin software.

The correct communications cable.

A general understanding of how UMX and UMP files work with the relay.

*Settable parameters will be overwritten by loading a new UMX file. They can however, be saved to a UMP file and then returned to the relay later. To learn how to do this, refer to the uMatrix Userguide.*

##### 5.1 Loading the UMX

Before loading the UMX2V164C software, ensure that it is compatible with your hardware. Download the compatibility list from the RMS website at:

[www.rmspl.com.au/digital/compatibility.pdf](http://www.rmspl.com.au/digital/compatibility.pdf)

Ensure that the bios version in the relay matches the table in section 2.0 of this document. (If the bios version is different, you may not be able to load this UMX. Contact RMS for support.)

Load the UMX via the front panel COM port using the 'uMatrixWin' software.

Ensure that the UMX version matches the table in section 2.0.

*To interrogate for versions, press 'SET' and 'DATA' buttons simultaneously, then select 'Version Page'. Alternatively, use uMatrixWin – Options – Utilities.*

Check versions

##### 5.2 Customer Default UMP

Load customer UMP file as per the Job Card. If no UMP file is requested, write N/A in check box.

UMP file name

The Relay is now ready for field use.

SOFTWARE INSTALLED BY :

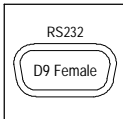
DATE :

## 6.0 CONNECTION DIAGRAM

\*Note: The status inputs & some relay outputs are assigned by the software (UMX) loaded on the relay.

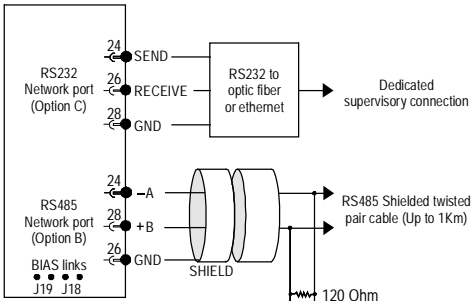
Shown here are the standard assignments of the A, B, C UMXs. Other UMX versions may differ. Consult the UMX data sheets for specific I/O assignments.

FRONT PANEL  
PC PROGRAMMING  
PORT



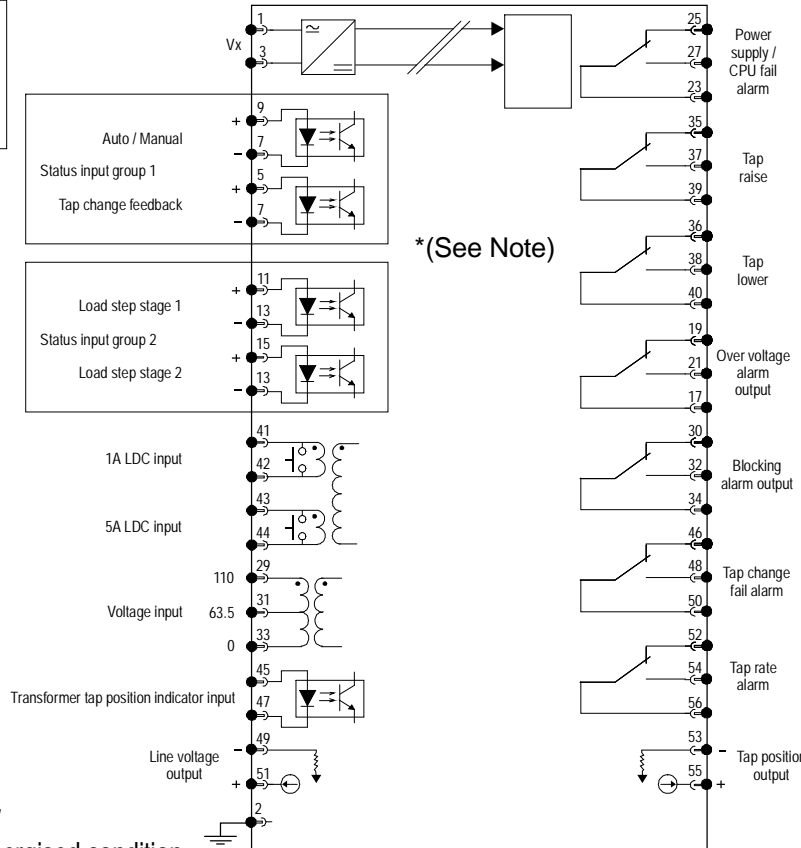
One DE09 straight through male to female  
2 metre connection cable supplied  
with each order of relays  
(P/N 997-000-042B)

SCADA  
COMMUNICATIONS  
PORT  
(Use one only)



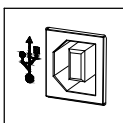
To other uMATRIX relays (Up to 32 units)  
Fit external terminating resistor to end of BUS relay only  
Fit internal BIAS jumper links for single relay connection only

2V164 wiring diagram\* - Relays shown in de-energised condition



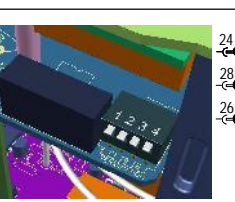
\*Note: The status inputs & some relay outputs are assigned by the software (UMX) loaded on the relay.

Shown here are the standard assignments of the A UMX. Other UMX versions may differ. Consult the UMX data sheets for specific I/O assignments.



FRONT PANEL  
USB PROGRAMMING  
PORT

SCADA  
COMMUNICATIONS  
PORT



To other uMATRIX relays (Up to 32 units)  
Invoke terminating 120R resistor to end of BUS relay only.  
To do this set SW100-3&4 ON.

Invoke BIAS resistors for single relay connection only.  
To do this set SW100-1&2 ON.

2V164-S wiring diagram - Relays shown in de-energised condition

